

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10672790
	Filing Date		2003-09-26
	First Named Inventor	John Banning	
	Art Unit	2183	
	Examiner Name	Coleman, E.	
	Attorney Docket Number	TRAN-P243	

U.S. PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5325495		1994-06-28	McLellan	
	2	5418970		1995-05-23	Gifford	
	3	5832205		1998-11-03	Kelly; et al	
	4	6041399		2000-03-21	Terada; et al	
	5	4312036		1982-01-19	Porter; et al	
	6	4314331		1982-02-02	Porter; et al	
	7	4371908		1985-01-01	Woods; et al	
	8	4371927		1983-02-01	Wilhite; et al	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

9	5057837		1991-10-15	Colwell; et al	
10	5129066		1992-07-07	Schmookler	
11	5179680		1993-01-12	Colwell; et al	
12	5301341		1994-04-05	Vassiliadis; et al	
13	5485629		1996-01-16	Dulong	
14	5649135		1997-07-15	Pechanek; et al	
15	5857103		1999-01-05	Grove	
16	5862398		1999-01-19	Hampapuram; et al	
17	5974539		1999-10-26	Gutttag; et al	
18	6065106		2000-05-16	Deao; et al	
19	6101592		2000-08-08	Pechanek; et al	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

20	6202142		2001-03-13	Narayan; et al	
21	6282633		2001-08-28	Killian; et al	
22	6317872		2001-11-13	Gee; et al	
23	6343373		2002-01-29	Kozumi; et al	
24	6397379		2002-05-28	Yates, Jr.; et al	
25	6442701		2002-08-27	Hurd	
26	6453407		2002-09-17	Lavi; et al	
27	6571386		2003-05-27	Figurin; et al	
28	6618804		2003-09-09	Steele, Jr.; et al	
29	4259718		1981-03-31	Kaman; et al	
30	5555428		1996-09-10	Radigan; et al	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

31	5115500		1992-05-19	Larsen	
----	---------	--	------------	--------	--

If you wish to add additional U.S. Patent citation information please click the Add button.

Add

U.S. PATENT APPLICATION PUBLICATIONS

Remove

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button.

Add

FOREIGN PATENT DOCUMENTS

Remove

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1							<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button.

Add

NON-PATENT LITERATURE DOCUMENTS

Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	Hennessy, John L. et al., Computer Architecture A Quantitative Approach, 1996, Morgan Kaufmann Publishers, Inc., 2nd Ed., Pages 130 and 284-289	<input type="checkbox"/>
	2	Ozer et al., A Fast Interrupt Handling Scheme for VLIW Processors, October 12-18, 1998, Proceedings of International Conference on Parallel Architectures and Compilation Techniques, Pages 136-141	<input type="checkbox"/>
	3	TITLE: A VLIW architecture for a trace scheduling compiler, author: Colwell et al, ACM, 1987	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

4	TITLE: Architecture and compiler tradeoffs for a long instruction wordprocessor, author: Cohn et al, ACM 1989	<input type="checkbox"/>
5	TITLE: Combining as a compilation technique for VLIW architectures, Author: Nakatani et al, ACM, 1989	<input type="checkbox"/>
6	TITLE: Core-Based RNS ALU with customized instructions, author: Ray, IEEE, 1990	<input type="checkbox"/>
7	TITLE: RISC I: a reduced instruction set VLSI computer, author: Patterson, ACM, 1988	<input type="checkbox"/>
8	FINAL OFFICE ACTION DATED 6-25-03; PATENT NO.: 6738892; TRAN-P021	<input type="checkbox"/>
9	NON-FINAL OFFICE ACTION DATED 4-11-02; PATENT NO.: 6738892; TRAN-P021	<input type="checkbox"/>
10	NON-FINAL OFFICE ACTION DATED 12-20-02; PATENT NO.: 6738892; TRAN-P021	<input type="checkbox"/>
11	FINAL OFFICE ACTION DATED 5-2-03; PATENT NO.: 6754892; TRAN-P033	<input type="checkbox"/>
12	NON FINAL OFFICE ACTION DATED 11-25-02; PATENT NO.: 6754892; TRAN-P033	<input type="checkbox"/>
13	NOTICE OF ALLOWANCE DATED 10-27-03; PATENT NO.: 6754892; TRAN-P033	<input type="checkbox"/>
14	NOTICE OF ALLOWANCE DATED 12-22-03; PATENT NO.: 6738892; TRAN-P021	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

15	ISSUE NOTIFICATION DATED 5-18-04; PATENT NO.: 6738892; TRAN-P021	<input type="checkbox"/>
16	ISSUE NOTIFICATION DATED 6-22-04; PATENT NO.: 6754892; TRAN-P033	<input type="checkbox"/>
17	NON FINAL OFFICE ACTION DATED 12-09-2005; TRAN-P072	<input type="checkbox"/>
18	NON FINAL OFFICE ACTION DATED 11-02-2006; TRAN-P072	<input type="checkbox"/>
19	NON FINAL OFFICE ACTION DATED 9-17-2007; TRAN-P072	<input type="checkbox"/>
20	AMENDMENT DATED 08-31-2006; TRAN-P072	<input type="checkbox"/>
21	NON FINAL OFFICE ACTION DATED 08-04-2006; TRAN-P072	<input type="checkbox"/>
22	FINAL OFFICE ACTION DATED 03-14-2007; TRAN-P072	<input type="checkbox"/>
23	FINAL OFFICE ACTION DATED 03-06-2008; TRAN-P072	<input type="checkbox"/>
24	FINAL OFFICE ACTION DATED 01-27-2009; TRAN-P072	<input type="checkbox"/>
25	Enterprise Systems Architecture/390 Principles of Operation. Eight Edition, July 2001. Pages 7-1 to 7-5, 9-1 to 9-4 and 9-8 to 9-9.	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10672790
Filing Date	2003-09-26
First Named Inventor	John Banning
Art Unit	2183
Examiner Name	Coleman, E.
Attorney Docket Number	TRAN-P243

26	ADVISORY ACTION DATED 07-05-2007; TRAN-P072	<input type="checkbox"/>
----	---	--------------------------

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.